

## Trench gate field-stop IGBT, M series 650 V, 30 A low-loss in a TO-220FP package

Datasheet - production data

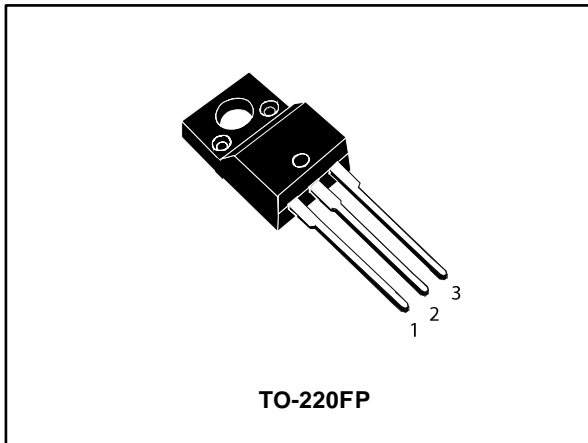
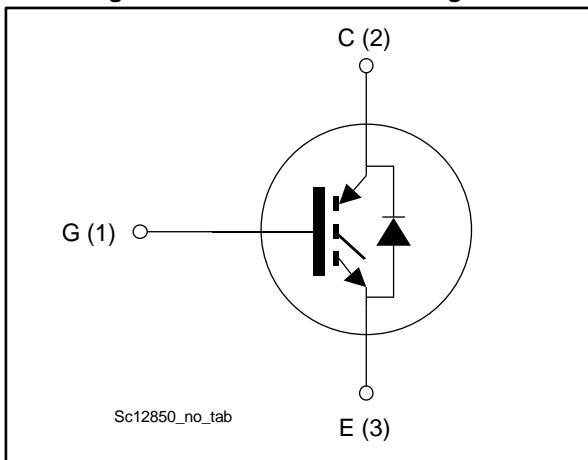


Figure 1: Internal schematic diagram



### Features

- 6  $\mu$ s of short-circuit withstand time
- $V_{CE(sat)} = 1.55$  V (typ.) @  $I_C = 30$  A
- Tight parameters distribution
- Safer paralleling
- Low thermal resistance
- Soft and very fast recovery antiparallel diode

### Applications

- Motor control
- UPS
- PFC

### Description

This device is an IGBT developed using an advanced proprietary trench gate field-stop structure. The device is part of the M series IGBTs, which represent an optimal balance between inverter system performance and efficiency where low-loss and short-circuit functionality are essential. Furthermore, the positive  $V_{CE(sat)}$  temperature coefficient and tight parameter distribution result in safer paralleling operation.

Table 1: Device summary

Order code	Marking	Package	Packing
STGF30M65DF2	G30M65DF2	TO-220FP	Tube

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## Contents

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# 1 Electrical ratings

Table 2: Absolute maximum ratings

Symbol	Parameter	Value	Unit
V <sub>CES</sub>	Collector-emitter voltage (V <sub>GE</sub> = 0 V)	650	V
I <sub>C</sub> <sup>(1)</sup>	Continuous collector current at T <sub>C</sub> = 25 °C	60	A
I <sub>C</sub> <sup>(1)</sup>	Continuous collector current at T <sub>C</sub> = 100 °C	30	A
I <sub>CP</sub> <sup>(2)</sup>	Pulsed collector current	120	A
V <sub>GE</sub>	Gate-emitter voltage	±20	V
I <sub>F</sub> <sup>(1)</sup>	Continuous forward current at T <sub>C</sub> = 25 °C	60	A
I <sub>F</sub> <sup>(1)</sup>	Continuous forward current at T <sub>C</sub> = 100 °C	30	A
I <sub>FP</sub> <sup>(2)</sup>	Pulsed forward current	120	A
V <sub>ISO</sub>	Insulation withstand voltage (RMS) from all three leads to external heat sink (t = 1 s, T <sub>C</sub> = 25 °C)	2.5	kV
P <sub>TOT</sub>	Total dissipation at T <sub>C</sub> = 25 °C	38	W
T <sub>STG</sub>	Storage temperature range	-55 to 150	°C
T <sub>J</sub>	Operating junction temperature range	-55 to 175	°C

**Notes:**

<sup>(1)</sup>Limited by maximum junction temperature.

<sup>(2)</sup>Pulse width limited by maximum junction temperature.

Table 3: Thermal data

Symbol	Parameter	Value	Unit
R <sub>thJC</sub>	Thermal resistance junction-case IGBT	4	°C/W
R <sub>thJC</sub>	Thermal resistance junction-case diode	5	°C/W
R <sub>thJA</sub>	Thermal resistance junction-ambient	62.5	°C/W

## 2 Electrical characteristics

$T_C = 25\text{ °C}$  unless otherwise specified

**Table 4: Static characteristics**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage	$V_{GE} = 0\text{ V}$ , $I_C = 250\text{ }\mu\text{A}$	650			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	$V_{GE} = 15\text{ V}$ , $I_C = 30\text{ A}$		1.55	2.0	V
		$V_{GE} = 15\text{ V}$ , $I_C = 30\text{ A}$ , $T_J = 125\text{ °C}$		1.95		
		$V_{GE} = 15\text{ V}$ , $I_C = 30\text{ A}$ , $T_J = 175\text{ °C}$		2.1		
$V_F$	Forward on-voltage	$I_F = 30\text{ A}$		1.85	2.65	V
		$I_F = 30\text{ A}$ , $T_J = 125\text{ °C}$		1.6		
		$I_F = 30\text{ A}$ , $T_J = 175\text{ °C}$		1.5		
$V_{GE(th)}$	Gate threshold voltage	$V_{CE} = V_{GE}$ , $I_C = 500\text{ }\mu\text{A}$	5	6	7	V
$I_{CES}$	Collector cut-off current	$V_{GE} = 0\text{ V}$ , $V_{CE} = 650\text{ V}$			25	$\mu\text{A}$
$I_{GES}$	Gate-emitter leakage current	$V_{CE} = 0\text{ V}$ , $V_{GE} = \pm 20\text{ V}$			$\pm 250$	$\mu\text{A}$

**Table 5: Dynamic characteristics**

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$C_{ies}$	Input capacitance	$V_{CE} = 25\text{ V}$ , $f = 1\text{ MHz}$ , $V_{GE} = 0\text{ V}$	-	2490	-	pF
$C_{oes}$	Output capacitance		-	143	-	
$C_{res}$	Reverse transfer capacitance		-	46	-	
$Q_g$	Total gate charge	$V_{CC} = 520\text{ V}$ , $I_C = 30\text{ A}$ , $V_{GE} = 0\text{ to }15\text{ V}$ (see <a href="#">Figure 30: "Gate charge test circuit"</a> )	-	80	-	nC
$Q_{ge}$	Gate-emitter charge		-	18	-	
$Q_{gc}$	Gate-collector charge		-	32	-	

Table 6: IGBT switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 400\text{ V}$ , $I_C = 30\text{ A}$ , $V_{GE} = 15\text{ V}$ , $R_G = 10\ \Omega$ (see <a href="#">Figure 29: "Test circuit for inductive load switching"</a> )		31.6	-	ns
$t_r$	Current rise time			13.4	-	ns
$(di/dt)_{on}$	Turn-on current slope			1791	-	A/ $\mu$ s
$t_{d(off)}$	Turn-off-delay time			115	-	ns
$t_f$	Current fall time			110	-	ns
$E_{on}^{(1)}$	Turn-on switching energy			0.3	-	mJ
$E_{off}^{(2)}$	Turn-off switching energy			0.96	-	mJ
$E_{ts}$	Total switching energy			1.26	-	mJ
$t_{d(on)}$	Turn-on delay time	$V_{CE} = 400\text{ V}$ , $I_C = 30\text{ A}$ , $V_{GE} = 15\text{ V}$ , $R_G = 10\ \Omega$ , $T_J = 175\text{ }^\circ\text{C}$ (see <a href="#">Figure 29: "Test circuit for inductive load switching"</a> )		30	-	ns
$t_r$	Current rise time			17	-	ns
$(di/dt)_{on}$	Turn-on current slope			1435	-	A/ $\mu$ s
$t_{d(off)}$	Turn-off-delay time			116	-	ns
$t_f$	Current fall time			194	-	ns
$E_{on}^{(1)}$	Turn-on switching energy			0.67	-	mJ
$E_{off}^{(2)}$	Turn-off switching energy			1.36	-	mJ
$E_{ts}$	Total switching energy			2.03	-	mJ
$t_{sc}$	Short-circuit withstand time	$V_{CC} \leq 400\text{ V}$ , $V_{GE} = 13\text{ V}$ , $T_{Jstart} = 150\text{ }^\circ\text{C}$	10		-	$\mu$ s
		$V_{CC} \leq 400\text{ V}$ , $V_{GE} = 15\text{ V}$ , $T_{Jstart} = 150\text{ }^\circ\text{C}$	6		-	

**Notes:**

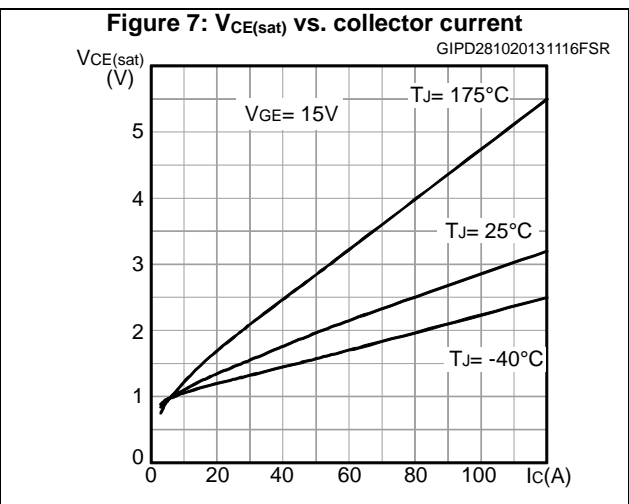
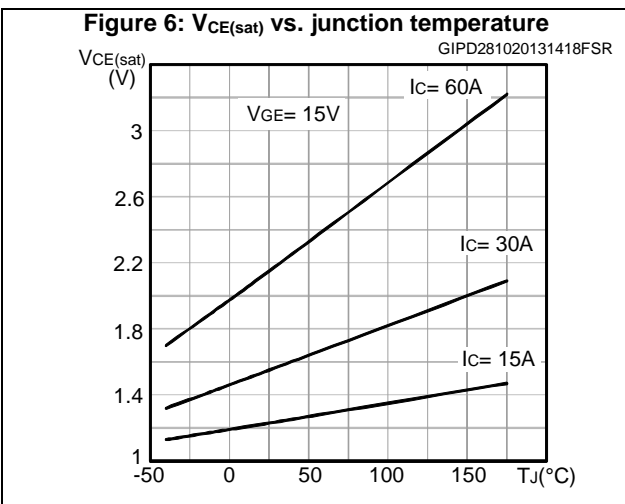
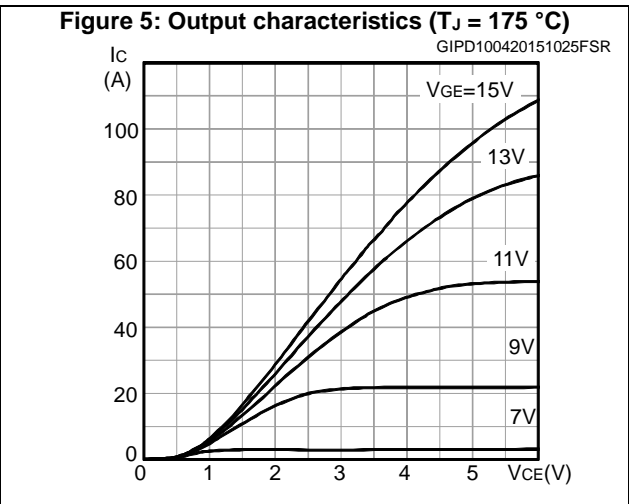
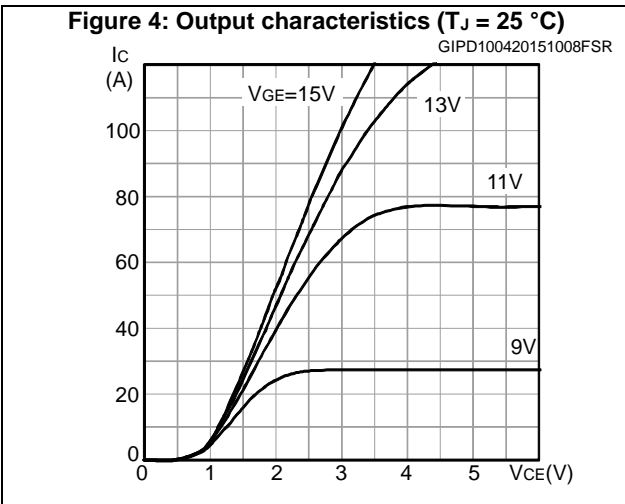
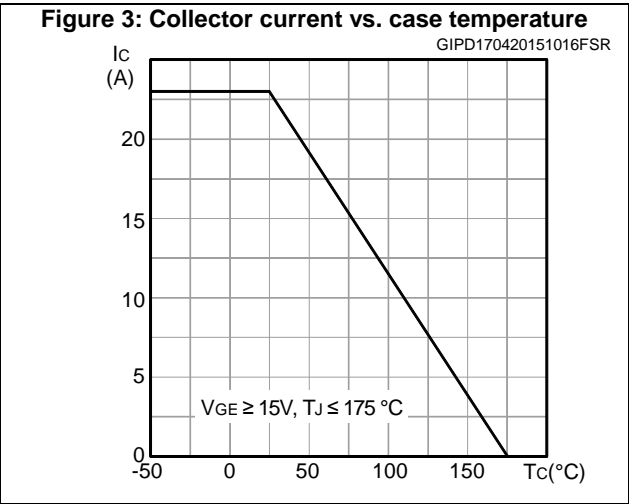
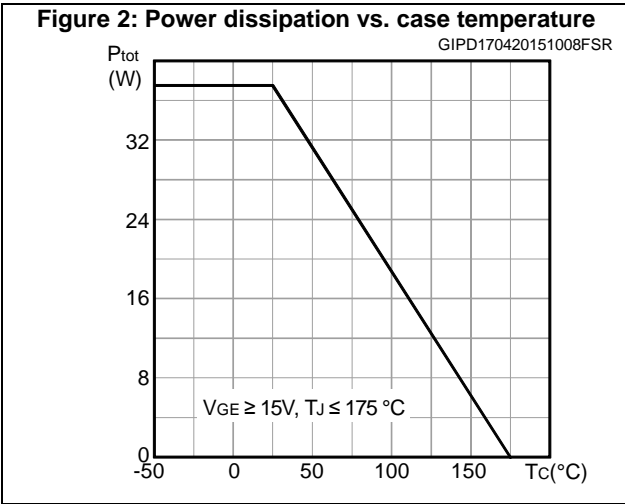
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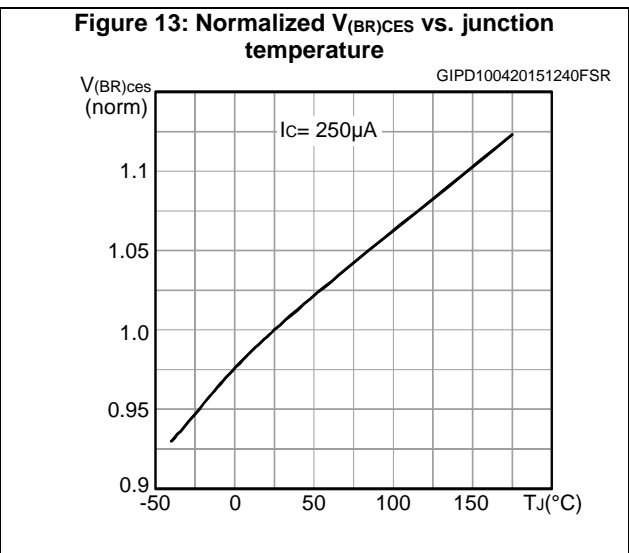
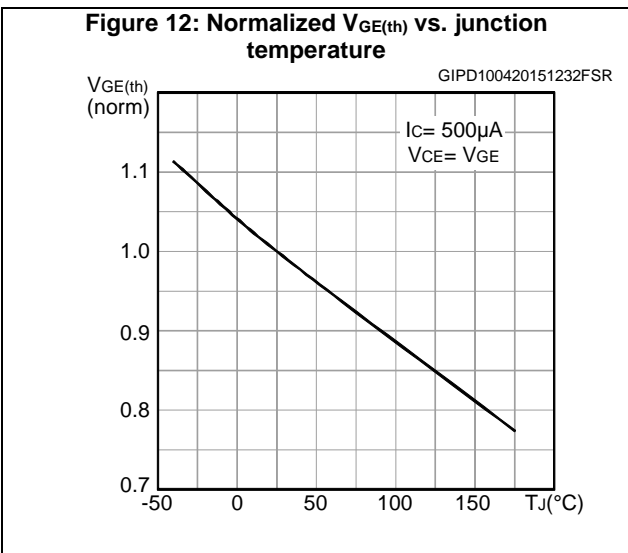
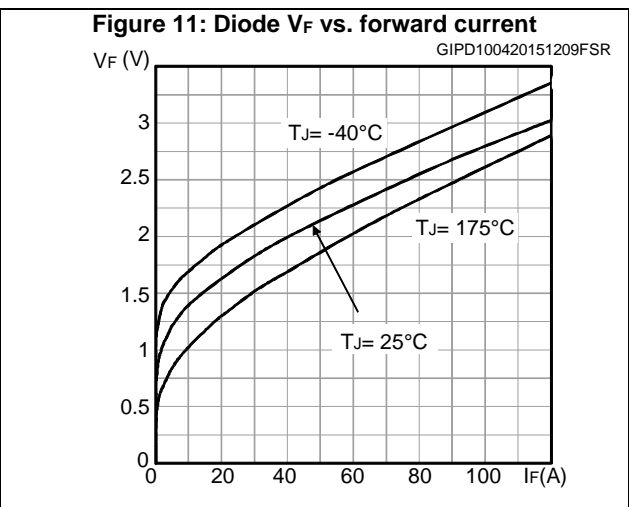
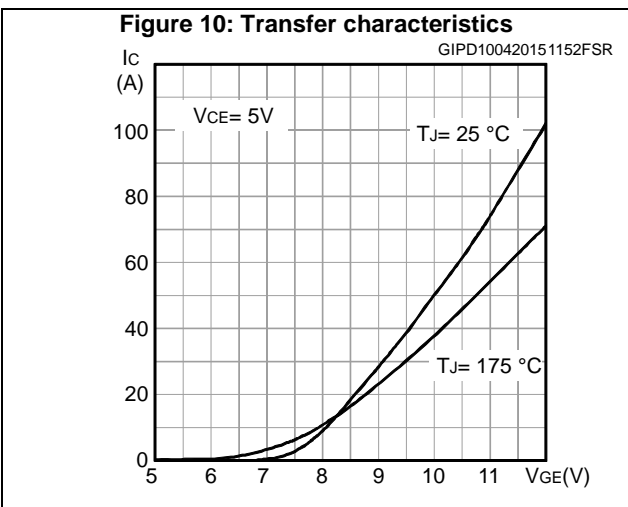
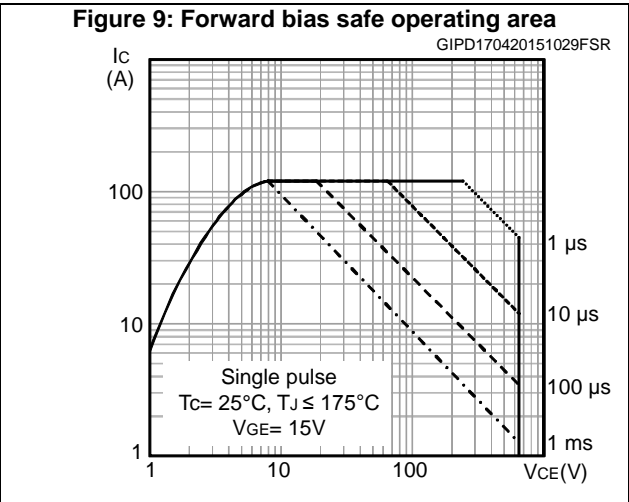
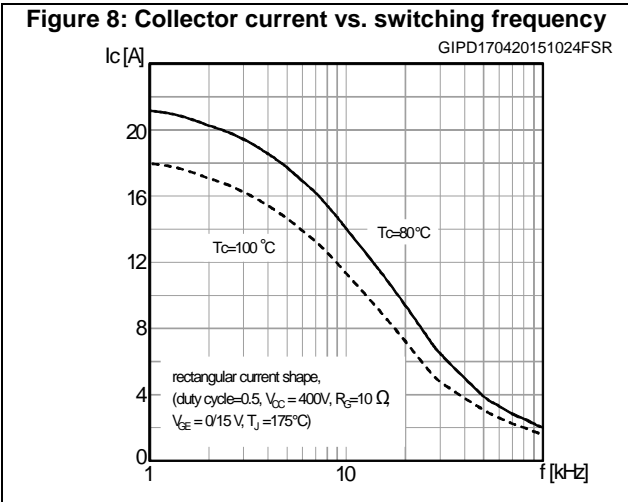
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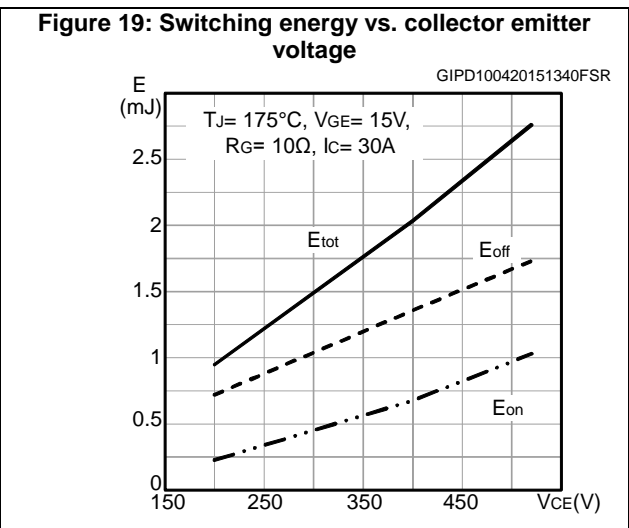
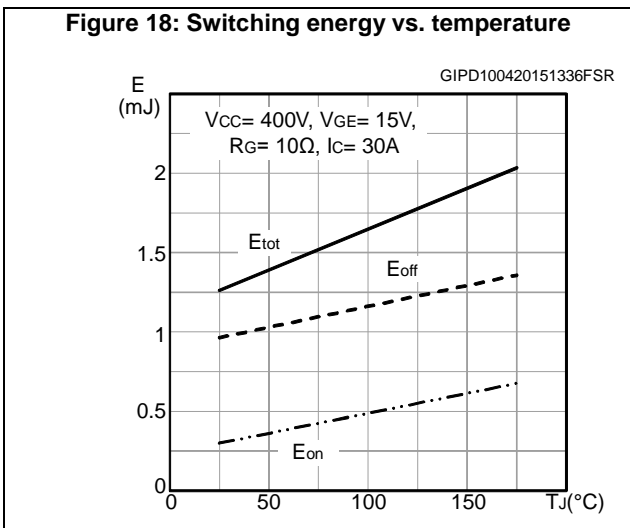
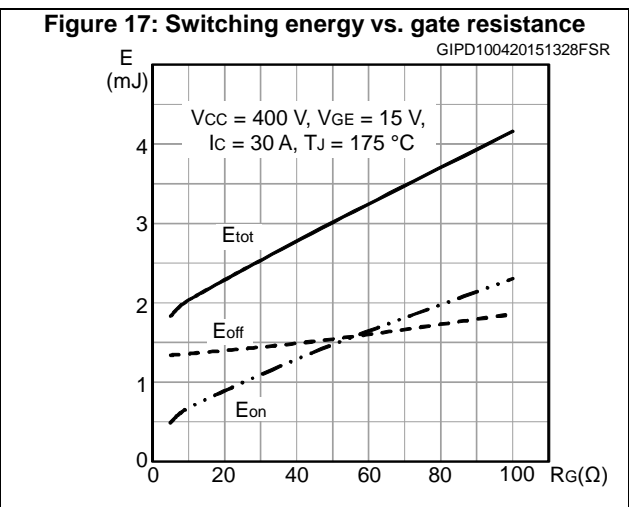
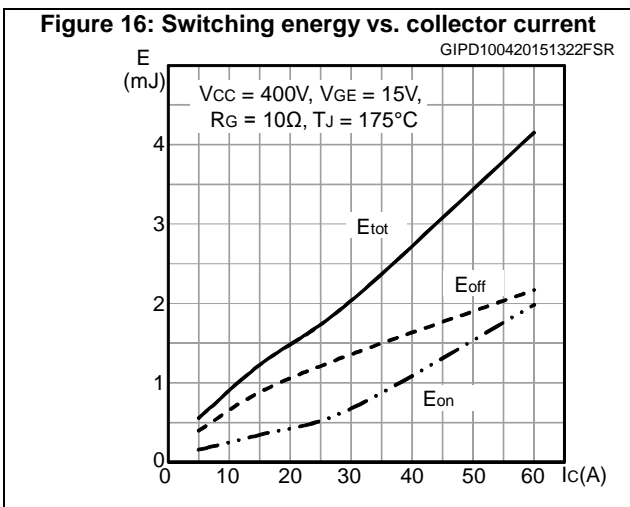
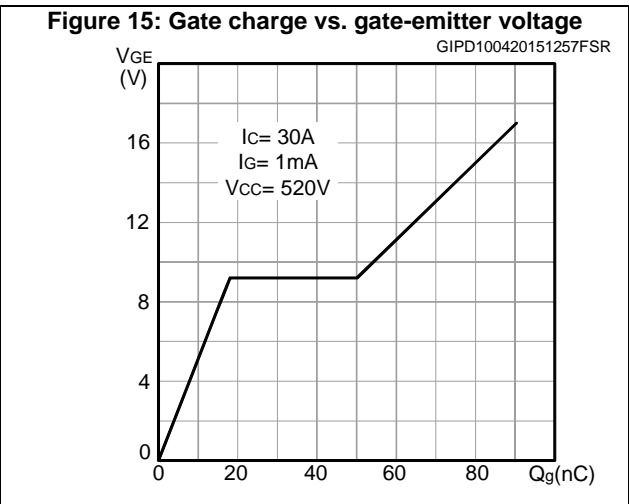
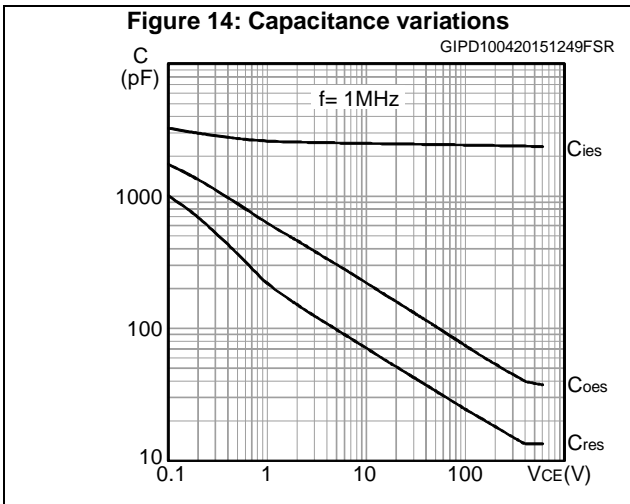
Table 7: Diode switching characteristics (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{rr}$	Reverse recovery time	$I_F = 30\text{ A}$ , $V_R = 400\text{ V}$ , $V_{GE} = 15\text{ V}$ , $di/dt = 1000\text{ A}/\mu\text{s}$ (see <a href="#">Figure 29: "Test circuit for inductive load switching"</a> )	-	140	-	ns
$Q_{rr}$	Reverse recovery charge		-	880	-	nC
$I_{rrm}$	Reverse recovery current		-	17	-	A
$dl_{rr}/dt$	Peak rate of fall of reverse recovery current during $t_b$		-	650	-	A/ $\mu$ s
$E_{rr}$	Reverse recovery energy		-	115	-	$\mu$ J
$t_{rr}$	Reverse recovery time	$I_F = 30\text{ A}$ , $V_R = 400\text{ V}$ , $V_{GE} = 15\text{ V}$ , $di/dt = 1000\text{ A}/\mu\text{s}$ , $T_J = 175\text{ }^\circ\text{C}$ (see <a href="#">Figure 29: "Test circuit for inductive load switching"</a> )	-	244	-	ns
$Q_{rr}$	Reverse recovery charge		-	2743	-	nC
$I_{rrm}$	Reverse recovery current		-	25	-	A
$dl_{rr}/dt$	Peak rate of fall of reverse recovery current during $t_b$		-	220	-	A/ $\mu$ s
$E_{rr}$	Reverse recovery energy		-	320	-	$\mu$ J

## 2.1 Electrical characteristics (curves)









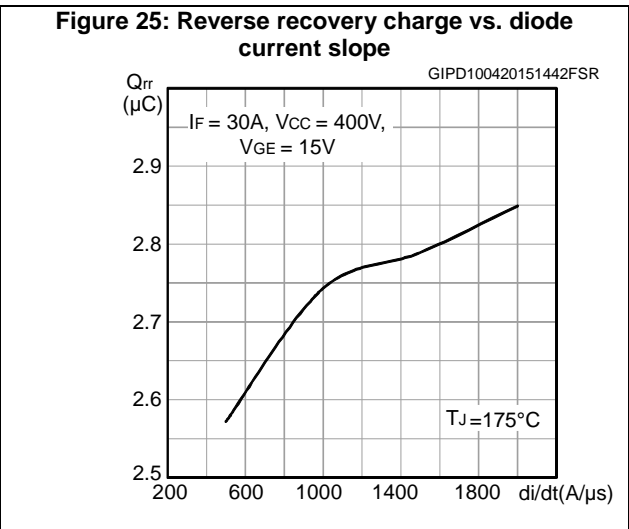
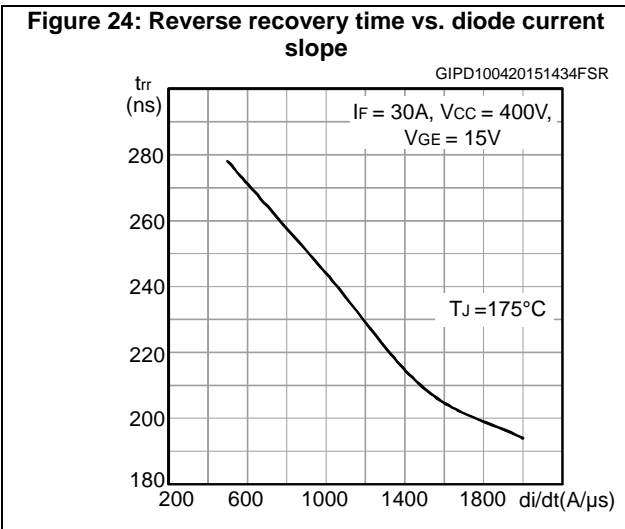
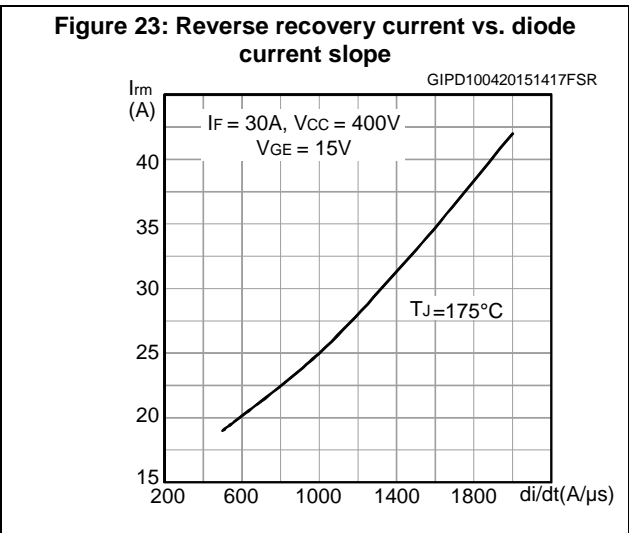
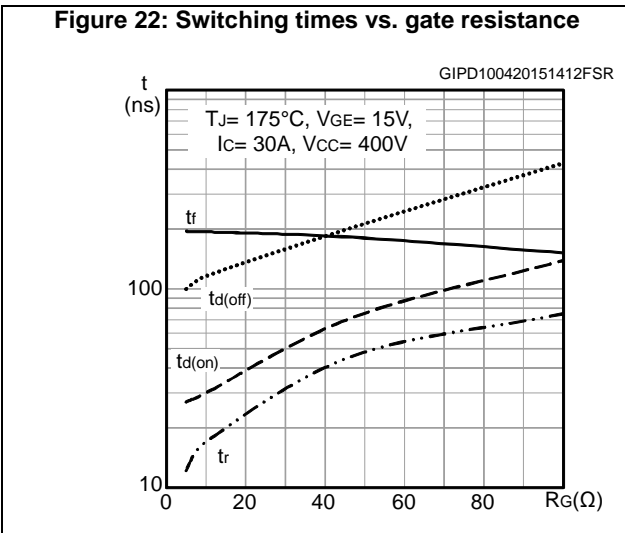
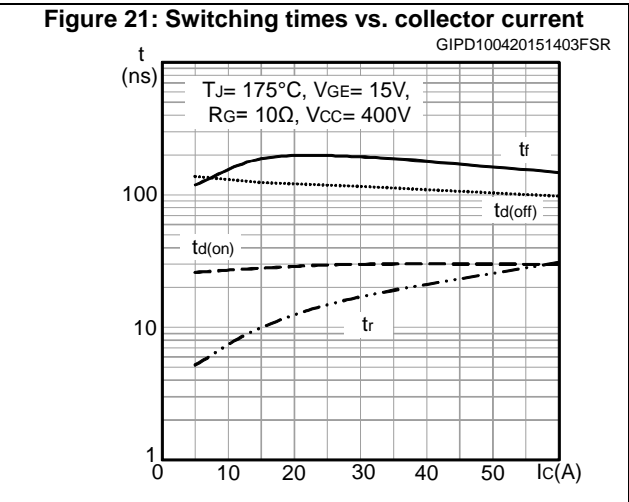
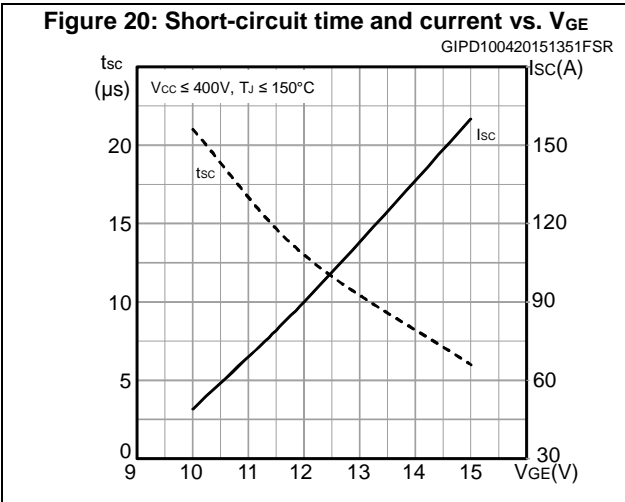


Figure 26: Reverse recovery energy vs. diode current slope

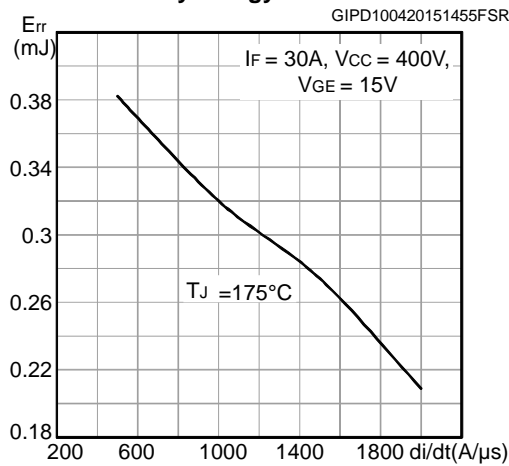


Figure 27: Thermal impedance for IGBT

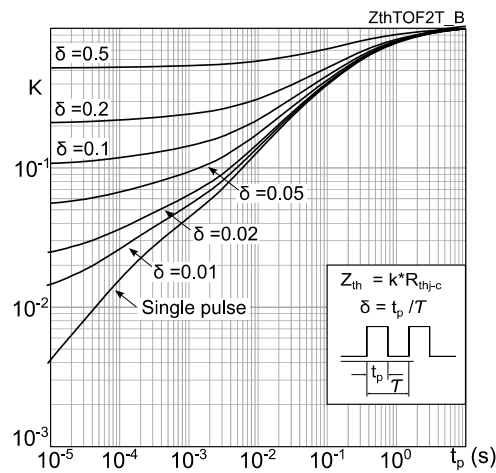
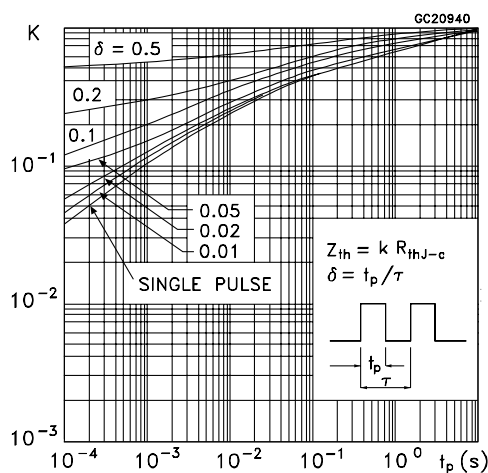
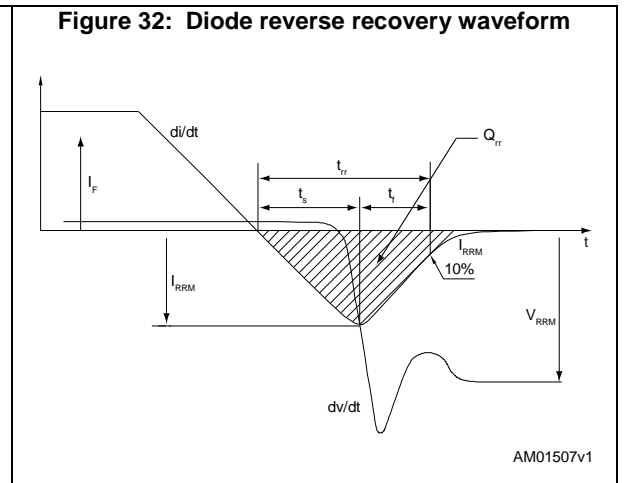
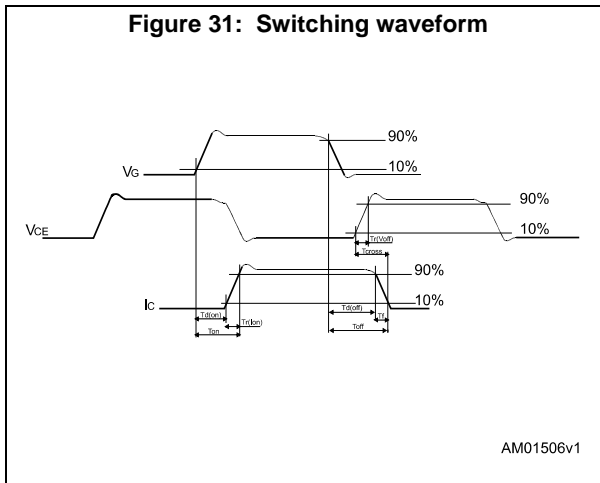
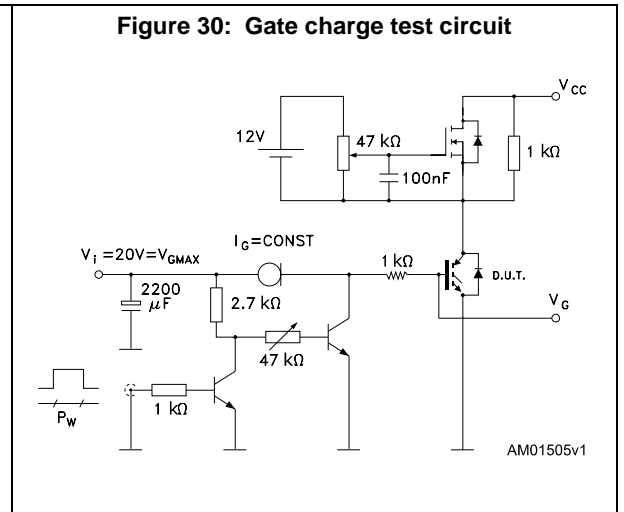
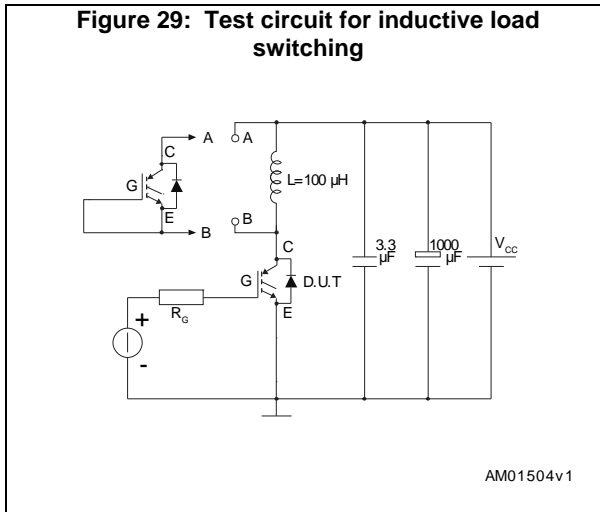


Figure 28: Thermal impedance for diode



### 3 Test circuits

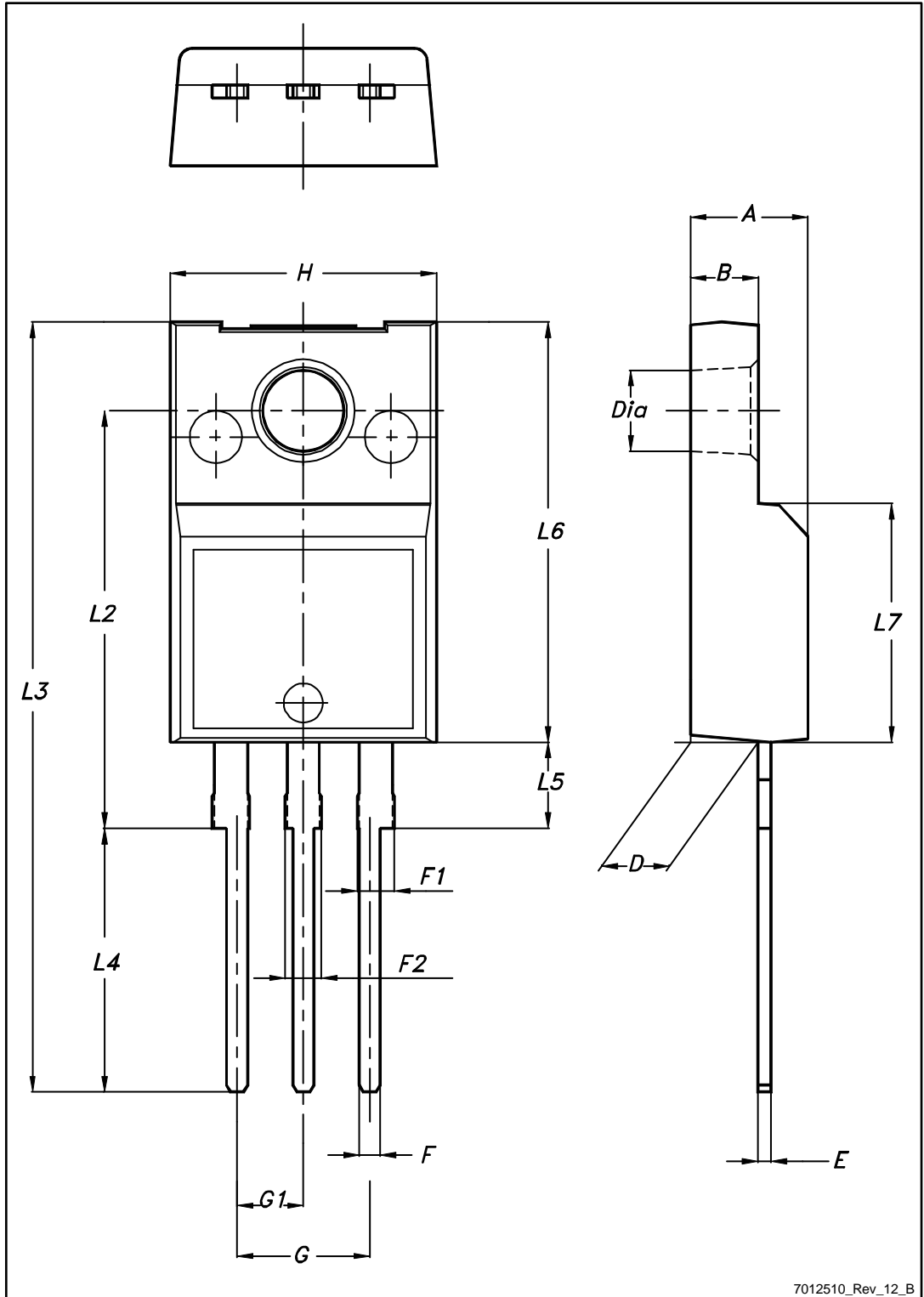


## 4 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: [www.st.com](http://www.st.com). ECOPACK® is an ST trademark.

### 4.1 TO-220FP package information

Figure 33: TO-220FP package outline



7012510\_Rev\_12\_B

Table 8: TO-220FP package mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	4.4		4.6
B	2.5		2.7
D	2.5		2.75
E	0.45		0.7
F	0.75		1
F1	1.15		1.70
F2	1.15		1.70
G	4.95		5.2
G1	2.4		2.7
H	10		10.4
L2		16	
L3	28.6		30.6
L4	9.8		10.6
L5	2.9		3.6
L6	15.9		16.4
L7	9		9.3
Dia	3		3.2

## 5 Revision history

Table 9: Document revision history

Date	Revision	Changes
28-Jan-2015	1	First release.
04-May-2015	2	Added <i>STGF30M65DF2</i> electrical characteristics curves.
19-Oct-2015	3	Changed <i>Figure 27: "Thermal impedance for IGBT"</i> .
08-Feb-2016	4	Datasheet promoted from preliminary data to production data Minor text changes
11-Apr-2017	5	Updated document title. Updated <i>Table 4: "Static characteristics"</i> , <i>Table 6: "IGBT switching characteristics (inductive load)"</i> and <i>Table 7: "Diode switching characteristics (inductive load)"</i> . Updated <i>Figure 13: "Normalized <math>V_{(BR)CES}</math> vs. junction temperature"</i> . Updated <i>Section 4.1: "TO-220FP package information"</i> . Minor text changes

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